



# CMOS Multichannel Single-Chip Receivers for Multi-Gigabit Optical Data Communications (Analog Circuits and Signal Processing)

*Paul Muller, Yusuf Leblebici*

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## **CMOS Multichannel Single-Chip Receivers for Multi-Gigabit Optical Data Communications (Analog Circuits and Signal Processing)** Paul Muller, Yusuf Leblebici

This book focuses on optical communications for short and very short distance applications and discusses the monolithic integration of optical receivers with processing elements in standard CMOS technologies. CMOS Multi-Channel Single-Chip Receivers for Multi-Gigabit Optical Data Communications provides the reader with the necessary background knowledge to fully understand the trade-offs in short-distance communication receiver design and presents the key issues to be addressed in the development of such receivers in CMOS technologies. Moreover, novel design approaches are presented. A system-level design methodology allows for the impact analysis of different block specifications and system-wide design optimization. Statistical models are used for design space exploration in the scope of jitter tolerance analysis of clock recovery circuits. CMOS Multi-Channel Single-Chip Receivers for Multi-Gigabit Optical Data Communications is required reading for practicing engineers and researchers in the field of short-distance optical communications and optical CMOS receiver design.

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